

**SIDDHARTH INSTITUTE OF ENGINEERING & TECHNOLOGY::PUTTUR****(Autonomous)**

(Approved by AICTE, New Delhi &amp; Affiliated to JNTUA, Anantapuramu)

(Accredited by NBA &amp; Accredited by NAAC with 'A+' Grade)

(An ISO 9001:2008 Certified Institution)

Siddharth Nagar, Narayanavanam Road, PUTTUR-517 583

**Examinations Branch****TIME TABLE****M.Tech I Year I Semester (R20) Supplementary Examinations July -2025****TIMINGS: 09:30 AM to 12:30 PM**

Date/Day	STRUCTURAL ENGINEERING	COMPUTER SCIENCE AND ENGINEERING	THERMAL ENGINEERING	POWER ELECTRONICS	VLSI
08-07-2025 (TUESDAY)	20HS0823 Research Methodology and IPR (Common to All)				
10-07-2025 (THURSDAY)	20CE1011 Structural Health Monitoring	20CS5001 Advanced Data Structures	20ME3101 Thermodynamics and Combustion	20EE2101 Electric Drives Systems	20EC4201 VLSI Technology
15-07-2025 (TUESDAY)	*****	20CS5002 Python Programming	*****	20EE2103 Advanced Power Electronic Circuits	20EC4202 Digital IC Design
17-07-2025 (THURSDAY)	*****	*****	20ME3112 Nuclear Engineering	*****	20EC4101 Embedded System Design
19-07-2025 (SATURDAY)	*****	*****	20ME3115 Air Conditioning System Design	*****	20EC4205 Verilog HDL

**Note:** If any omissions or clashes in this time table may please be informed to the undersigned immediately.  
**CONTROLLER OF EXAMINATIONS****Controller of Examinations**  
Siddharth Institute of Engineering & Technology  
(AUTONOMOUS),  
PUTTUR - 517583, Tirupati Dist. A.P.  
**PRINCIPAL**  
Siddharth Institute of Engineering & Technology  
(AUTONOMOUS), Siddharth Nagar,  
PUTTUR - 517 583, Tirupati Dist. A.P.